

GX3601



3U FPGA PXI CARD WITH 80 CHANNEL TTL BUFFER MODULE (GX3500 & GX3501)

- User programmable PXI card with 80 TTL / LVTTTL buffered channels
- Read / Write control via a software front panel or API
- Direction control configurable on a per pin basis



DESCRIPTION

The GX3601 is a 3U PXI FPGA card with 80, TTL buffered channels. The card is comprised of a GX3501 80 channel, TTL buffer expansion module and a GX3500 FPGA card.

PROGRAMMING AND SOFTWARE

The board is supplied with the GXFPGA library, a software package that includes a virtual instrument panel, and a Windows 32/64-bit DLL driver library and documentation. The virtual panel can be used to interactively program and control the instrument from a window that displays the instrument's current settings and status. In addition, interface files are provided to support access to programming tools and languages such as ATEasy, LabView, LabView/Real-Time, C/C++, Microsoft Visual Basic®, Delphi, and Pascal. An On-Line help file and PDF User's Guide provides documentation that includes instructions for installing, using and programming the board.

A separate software package - [GtLinux](#) - provides support for Linux 32/64 operating systems.

APPLICATIONS

- Automatic Test Equipment (ATE) and Functional Test
- Data Acquisition
- Process Control
- Factory Automation

SPECIFICATIONS

GX3500 FPGA CARD WITH GX3501 TTL BUFFER EXPANSION BOARD	
Number of Channels	80 I/O signals. Direction is configurable by software on a per pin basis
Logic Family	TTL or LVTTTL, 5 V tolerant inputs; Each group of 40 channels can be jumper configured to support TTL or LVTTTL levels
Output Current	±50 mA, sink or source
Input Leakage Current	±5 µA
Power On State	All channels are configured as inputs
Input Protection	Overvoltage: -0.5 V to 6.5 V (input)

Note: Specifications are subject to change without notice

ORDERING INFORMATION

GX3601	FPGA PXI Card with 80 Channel TTL Buffer Module
GX3601-M	FPGA PXI Card with 80 Channel TTL Buffer Module (Ruggedized & Conformally Coated)

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