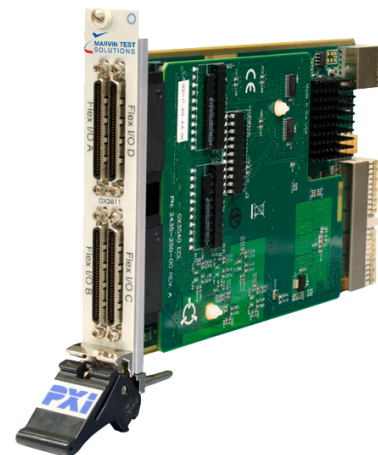


GX3611



3U FPGA PXI CARD WITH 80 CHANNEL LVDS BUFFER MODULE (GX3500 & GX3511)

- FPGA PXI card with 80 LVDS channels
- Supports data rates up to 400 Mb/s
- Read / Write control via a software front panel or API
- Direction configurable on a per pin basis
- PXI hybrid slot compatible



DESCRIPTION

The GX3611 is a 3U PXI FPGA card with 80 LVDS channels and is comprised of the GX3500 PXI FPGA card and the GX3511 expansion card. The card provides 80 LVDS transceiver channels. Each channel includes a 100 ohm termination.

PROGRAMMING AND SOFTWARE

The board is supplied with the GXFPGA library, a software package that includes a virtual instrument panel, and a Windows 32/64-bit DLL driver library and documentation. The virtual panel can be used to interactively program and control the instrument from a window that displays the instrument's current settings and status. In addition, interface files are provided to support access to programming tools and languages such as ATEasy, LabView, LabView/Real-Time, C/C++, Microsoft Visual Basic®, Delphi, and Pascal. An On-Line help file and PDF User's Guide provides documentation that includes instructions for installing, using and programming the board.

A separate software package - [GtLinux](#) - provides support for Linux 32/64 operating systems.

APPLICATIONS

- Automatic Test Equipment (ATE) and Functional Test
- Data Acquisition
- Process Control
- Factory Automation

GX3611



SPECIFICATIONS

GX3500 FPGA CARD WITH GX3511 LVDS EXPANSION BOARD	
Number of Channels	80 I/O signals. Direction is configurable by software on a per pin basis
Differential Channel Termination	100
Logic Family	LVDS, compliant with TIA/EIA-644 devices
Data Rate	400 Mbps (max)
Differential Output Voltage	247 mV (min); 454 mV (max), Rload = 50
Differential Input Voltage Threshold	50 mV (max), positive going input -50 mV (max), negative going input
Absolute Maximum Input Voltage Range	-0.5 V to 4 V
Power on State	All channels are configured as inputs
Connector	(4) SCSI III, VHDCI type, 68 pin female
TIMING SOURCES	
PXI Bus	10 MHz
Internal	80 MHz oscillator, ± 20 ppm
PCI Clock	33 MHz
FPGA	
FPGA Type	Altera Cyclone III, EP3C55 F484
Number of PLLs	Four
Logic Elements	55,856
Internal Memory	2.34 Mb
POWER	
3.3 VDC	2.2 A (max)
5 VDC	1.2 A (max)

ENVIRONMENTAL	
Operating Temperature	0 °C to +50 °C
Storage Temperature	-20 °C to +70 °C
Size	3U PXI
Weight	200 g

Note: Specifications are subject to change without notice

ORDERING INFORMATION

GX3611	3U FPGA PXI Card, LVDS, 80 Differential Channels (GX3500 & GX3511)
ACCESSORY	
GT95015	Connector Interface for all 5xxx/35xx, SCSI to 100 Mil Grid, Differential
GT95034	4 ft. Shielded cable with 68 Pin SCSI connectors for GX3611 / GX3610 / GX3609
GT95034-6	6 ft. Shielded cable with 68 Pin SCSI connectors for GX3611 / GX3610 / GX3609