

GX3501



80 CHANNEL TTL BUFFER EXPANSION BOARD FOR GX3500

- 80 channel, TTL buffer board for the GX3500 FPGA card
- Configurable for TTL or LVTTTL logic levels
- Each channel configurable as input or output



DESCRIPTION

The GX3501 is an 80 channel, TTL buffer expansion card designed specifically for the GX3500 FLeX DIO FPGA card. Each group of 40 channels can be configured with an on-board jumper to support TTL or LVTTTL logic levels. Each channel can be configured to an input or output under software control. The GX3501 is also available to be ordered as a pre-assembled unit with the GX3500 as part number GX3601.

SPECIFICATIONS

GX3501 TTL BUFFER EXPANSION BOARD	
Number of Channels	80 I/O signals. Direction is configurable by software on a per pin basis
Logic Family	TTL or LVTTTL, 5 V tolerant inputs; Each group of 40 channels can be jumper configured to support TTL or LVTTTL levels
Output Current	±50 mA, sink or source
Input Leakage Current	±5 µA
Power On State	All channels are configured as inputs
Input Protection	Overtolerance: -0.5 V to 6.5 V (input)

Note: Specifications are subject to change without notice

ORDERING INFORMATION

GX3501	FPGA Flex Daughtercard: 80 Channel TTL Buffer
REQUIRED	
GX3500	FPGA PXI Digital I/O Card

DIGITAL I/O

GX3501



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