

GX3540



40 CHANNEL ECL EXPANSION BOARD FOR GX3500

- 40 input / output ECL channels
- Configurable terminations to -5.2 V or -2 V
- Requires the GX3500 FlexDIO FPGA card



DESCRIPTION

The GX3540 is a 40 Channel MECL Buffer expansion card designed specifically for the GX3500 FLeX DIO FPGA card. The GX3540 can also be supplied pre-assembled to the GX3500 as model number GX3640.

ORDERING INFORMATION

GX3540	FPGA Flex Daughtercard: 40 Channel MECL
I/O MODULE	
GX3500	FPGA PXI Digital I/O Card

SPECIFICATIONS

GX3540 ECL EXPANSION BOARD	
Number of Channels	20 differential inputs 20 differential outputs
Logic Family	MECL 10 K compatible
Driver Output VOH	-0.98 V (min) -0.81 V (max)
Driver Output VOL	-1.95 V (min) -1.63 V (max)
Receiver Input VIH	-1.13 V (min) -0.81 V (max)
Receiver Input VIL	-1.95 V (min) -1.48 V (max)
Receiver Differential Input Level	150 mV (typ) 1.0 V (max)
Receiver Common Mode Range	-2.85 V (min) +0.3 V (max)
Maximum Absolute Input Voltage Range	0 to -5.2 V
ECL I/O Terminations	Each input and output is terminated with a 390 resistor which is connected to -5.2 V. Optionally, the board can be configured to use terminations with an onboard -2 V termination voltage source
I/O Connectors	(2) SCSI III, VDHCI type, 68 pin female One for ECL outputs, one for ECL inputs

Note: Specifications are subject to change without notice



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