

Declaration of Volatility

Products

Model Number	Description
GX3601 (GX3500 & GX3501)	Digital I/O FPGA PXI Card with 80 Channel TTL Buffer Module
GX3609 (GX3500 & GX3509)	Digital I/O FPGA PXI Card with 80 Channel TTL Differential Module
GX3610 (GX3500 & GX3510)	Digital I/O FPGA PXI Card with 80 Channel mLVDS Buffer Module
GX3640 (GX3500 & GX3540)	Digital I/O FPGA PXI Card with 40 Channel ECL Buffer Module

Volatile Memory

This product contains the following volatile memory, which is erased every time power is removed from the product.

Type	Size	User Accessible	Battery Backup	Purpose	Clearing Method
FPGA SRAM	117 Kb / 2,340 Kb	User Defined	No	PXI Bus Interface, User FPGA	Cycle power (JP7 Open)

Non-Volatile Memory

This product contains the following non-volatile memory, which the contents are retained when power is removed from the product.

Type	Size	User Accessible	Battery Backup	Purpose	Clearing Method
FLASH ^{1,3}	1 Mb	No	No	I/F FPGA Configuration	Not Available to User
FLASH	16 Mb	No	No	User FPGA Configuration	User Defined
EEPROM ^{2,4}	128 Kb	No	No	Board Information	Not Available to User

Media Storage

This product contains no media storage devices.

Type	Size	User Accessible	Battery Backup	Purpose	Clearing Method
None	N/A	N/A	N/A	N/A	N/A

Notes:

1. Altering the contents of this memory may render the product unusable.
2. Calibration constants that are stored in device EEPROMs include information for the device's full operating temperature range. Calibration constants do not maintain any unique data for specific configurations at which the device is used unless otherwise specified.
3. Hardware changes or a unique tool from Marvin Test Solutions are required to modify contents of the memory listed.
4. Not available to user indicates that the ability to clear this memory is not available to the end user under normal operating conditions. The utility required to clear the memory are not distributed by Marvin Test Solutions to customers for normal use.